Claim Amendments

1. (Currently Amended) A method, comprising:

writing data fragments to a non-volatile memory; ~~and~~

updating entries of a sequence table ~~entries~~ to a volatile memory that identify locations of the data fragments written to the non-volatile memory; and

writing the sequence table from the volatile memory to the non-volatile memory at least one of when the sequence table is full and when writing the data fragments to the non-volatile memory is completed.

2-4. (Canceled)

5. (Currently Amended) ~~A~~ The method of claim 1, further comprising:

updating a transaction indicator in the non-volatile memory prior to writing a transaction to the non-volatile memory; and

updating the transaction indicator in the non-volatile memory after writing the transaction to the non-volatile memory,

wherein the transaction comprises the data fragments and the sequence table ~~entries~~.

6. (Currently Amended) ~~A~~ The method of claims 1, further comprising:

allocating a data fragment header ~~associated to the data fragment~~ in the non-volatile memory prior to writing a data fragment of the data fragments to the non-volatile memory, wherein the data fragment header is associated to the data fragment; and

validating the data fragment header after writing the sequence table ~~entries~~ to the non-volatile memory.

7. (Currently Amended) ~~A~~ The method of claim 1, comprising:

allocating a sequence table header ~~associated with the sequence table~~ in the non-volatile memory prior to writing the sequence table to the non-volatile memory, wherein the sequence table header is associated with the sequence table; and

validating the sequence table header after writing the sequence table ~~entries~~ to the non-volatile memory.

8. (Currently Amended) A system, comprising:

a non-volatile memory;

a volatile memory; and

a processor to:

write data fragments to a non-volatile memory; ~~and~~

update entries of a sequence table ~~entries~~ to a volatile memory that identify locations of the data fragments written to the non-volatile memory; and

write the sequence table from the volatile memory to the non-volatile memory at least one of when the sequence table is full and when writing the data fragments to the non-volatile memory is completed.

9-11. (Canceled)

12. (Currently Amended) ~~A~~ The system of claim 8, wherein the processor further:

updates a transaction indicator in the non-volatile memory prior to writing a transaction to the non-volatile memory; and

updates the transaction indicator in the non-volatile memory after writing the transaction to the non-volatile memory,

wherein the transaction comprises the data fragments and the sequence table ~~entries~~.

13. (Currently Amended) ~~A~~ The system of claims 8, wherein the processor further:

allocates a data fragment header ~~associated to the data fragment~~ in the non-volatile memory prior to writing a data fragment of the data fragments to the non-volatile memory, wherein the data fragment header is associated to the data fragment; and

validates the data fragment header after writing the sequence table ~~entries~~ to the non-volatile memory.

14. (Currently Amended) ~~A~~ The system of claim 8, wherein the processor further:

allocates a sequence table header ~~associated with a sequence table~~ in the non-volatile memory prior to writing the sequence table to the non-volatile memory, wherein the sequence table header is associated with the sequence table; and

validates the sequence table header after writing the sequence table ~~entries~~ to the non-volatile memory.

15. (Currently Amended) A machine-readable ~~medium~~ storage device comprising a plurality of instructions which when executed result in an apparatus:

writing data fragments to a non-volatile memory; ~~and~~

updating entries of a sequence table ~~entries~~ to a volatile memory that identify locations of the data fragments written to the non-volatile memory; and

writing the sequence table from the volatile memory to the non-volatile memory at least one of when the sequence table is full and when writing the data fragments to the non-volatile memory is completed.

16-18. (Canceled)

19. (Currently Amended) The machine-readable ~~medium~~ storage device of claim 15 wherein the plurality of instructions further result in the apparatus:

updating a transaction indicator in the non-volatile memory prior to writing a transaction to the non-volatile memory; and

updating the transaction indicator in the non-volatile memory after writing the transaction to the non-volatile memory,

wherein the transaction comprises the data fragments and the sequence table ~~entries~~.

20. (Currently Amended) The machine-readable ~~medium~~ storage device of claim 15 wherein the plurality of instructions further result in the apparatus:

allocating a data fragment header ~~associated to the data fragment~~ in the non-volatile memory prior to writing a data fragment of the data fragments to the non-volatile memory, wherein the data fragment header is associated to the data fragment; and

validating the data fragment header after writing the sequence table ~~entries~~ to the non-volatile memory.

21. (Currently Amended) The machine-readable ~~medium~~ storage device of claim 15 wherein the plurality of instructions further result in the apparatus:

allocating a sequence table header ~~associated with a sequence table~~ in the non-volatile memory prior to writing the sequence table to the non-volatile memory, wherein the sequence table header is associated with the sequence table; and

validating the sequence table header after writing the sequence table ~~entries~~ to the non-volatile memory.